# AN10992 Quick Startup Guide for RD710 Rev. 2.1 — 6 December 2011

**Application note COMPANY PUBLIC** 

#### **Document information**

Info	Content
Keywords	RD710, RD852, Quick startup guide, SAM AV1, SAM AV2, RC523, RX852
Abstract	This document is intended for new users to start working with the Design- In Kit. It shows the basic functionality with MIFAREdiscover.



# **Quick Startup Guide for RD710**

# **Revision history**

Rev	Date	Description
2.1	20111206	Added in 5.1 the Activation of a MIFARE Classic Card.
2.0	20110803	Extended usecases for MIFAREdiscover
1.1	20110712	Update due to release of new usecases of MIFAREdiscover
1.0	20110411	Initial version
	20110105	Draft version

# **Contact information**

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# 1. Introduction

The purpose of this document is to provide a set of guidelines to aid in the first operation of the RD710 reader. The MIFAREdiscover (ver.3.x.x) will be used as a user interface to communicate to the readers and cards, respectively.

The USB drivers are included in the CD package.

# 2. Installation

# 2.1 Required items

To use the MIFAREdiscover, the following items are required:

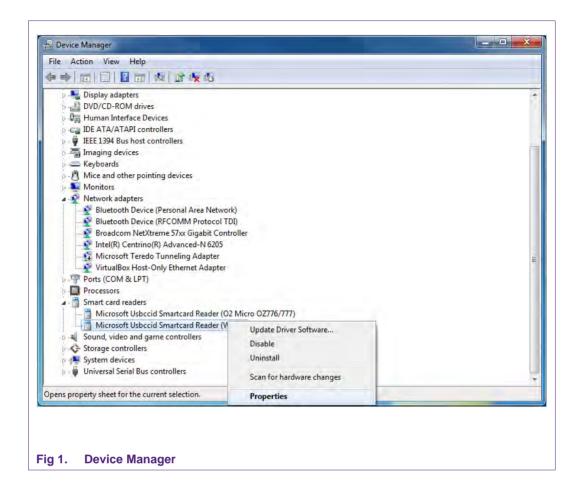
- MIFARE cards as MIFARE DESFire EV1, MIFARE Classic, MIFARE Ultralight
- Pegoda Reader (RD710 or RD710 as part of EV710) (see [32] [36])
- MIFAREdiscover (see [37])
  - o Public version available on NXP web (see [39])
  - o Full version (see [40])

# 2.2 Installing USB driver for the Reader

The demonstrated installation is shown on Windows 7, but it is the same in Windows XP and Vista.

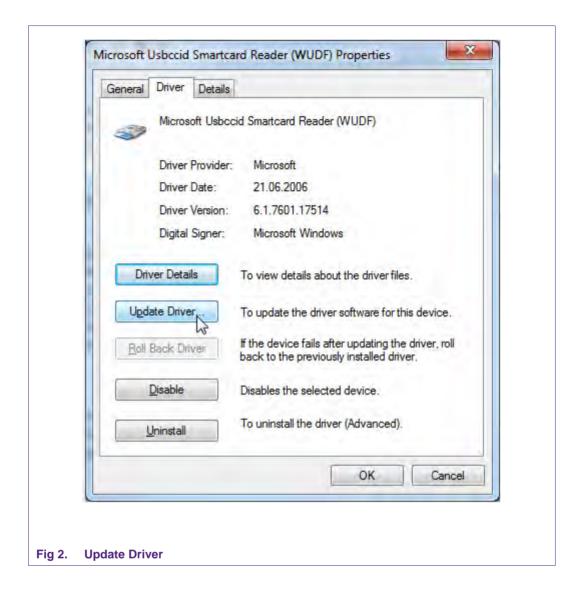
- 1) If you don't have a CD that was delivered with your Pegoda, open your web browser, go to see [38] and download and extract the zip file.
- 2) Connect the Pegoda RD710 with your computer.
- 3) Wait until Windows 7 installs a standard driver.
- 4) Open the Windows Device Manager and navigate to the installed Reader (see figure 1 below)

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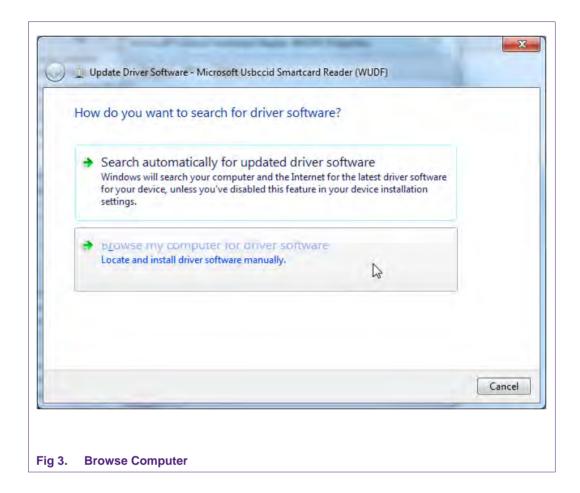
- 5) Click Smartcard Reader with the right mouse button and choose "Properties".
- 6) Choose the tab driver and click "Update Driver".

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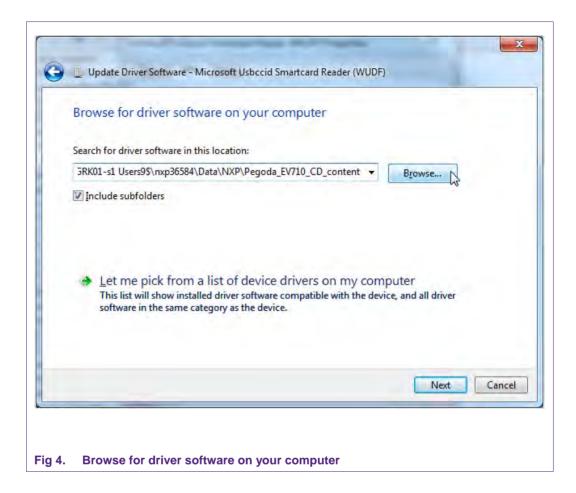
7) Windows will ask you how to search for the driver. Choose "Browse my computer for driver software".

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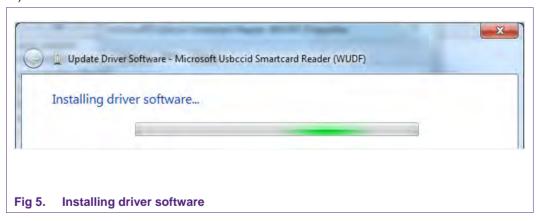


8) Choose "Browse", navigate to the root directory of the CD or the previous extracted content and click "Next".

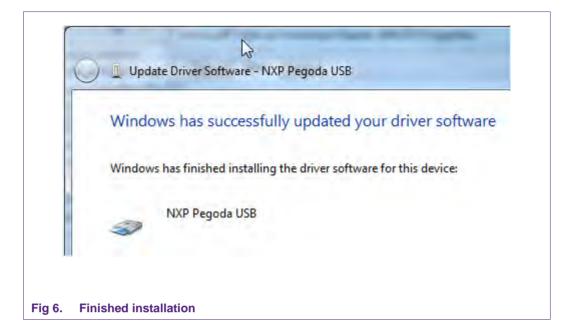
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9) Wait until Windows has finished the installation.



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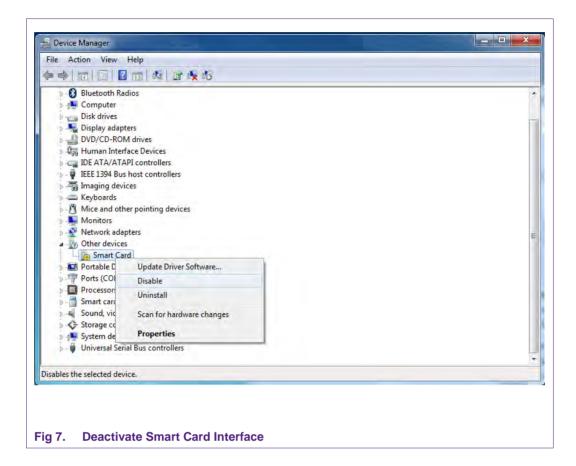
10) The installation is done.

# 2.3 Deactivate Smart Card Interface

For some customers it may be useful to deactivate the Smart Card Interface. This is especially important for users of Windows 7. If you see that Windows repeatedly tries to install a new Smart Card Interface you need to do the following steps.

- 1. Go to the Control Panel of your computer (Start Control Panel)
- 2. Click "System" "Device Manager" and then "Other devices"
- 3. Click "Smart Card" with your right mouse button and then disable.

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# 2.4 Installing MIFAREdiscover

There are two different versions of MIFAREdiscover; for once the full version can be retrieved from the doc store and the public version, which can be downloaded from the NXP Homepage.

The public MIFARE discover supports the functions for MIFARE Classic (see [1]) MIFARE Ultralight (see [13]), General ISO 14443-A (see [8]) protocol handling.

The full MIFAREdiscover supports the functions for MIFARE SAM AV2 (see [18]-[31]) support (X and conventional), MIFARE Plus (see [5]) MIFARE DESFire EV1 (see [4]), MIFARE Classic, MIFARE Ultralight, MIFARE Ultralight C (see [6]) and General ISO14443-A protocol handling.

# 2.4.1 System Requirements

- · Microsoft Windows XP SP2 or higher
- Minimum screen resolution 1024x768 pixels
- Microsoft .NET Framework 3.5 Service Pack 1 or higher [will be installed along with this installer]
- Pegoda
- MIFARE SAM AV2 for X-mode

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# 2.4.2 Installation process

Install Microsoft .NET Framework 3.5 SP1 (or higher if available)

- The Installer "SetupMIFAREdiscover" tries to install the Microsoft .NET Framework 3.5 Service Pack 1 by using a Net-Installer. If you have limited or no network connection to download and install the Microsoft .NET Framework the setup process is terminated and you have to install the Microsoft .NET Framework manually.
- .NET Framework can be found online (see [41])
- Install the "SetupMIFAREdiscover" package:

Install the package and follow the instructions. The whole installation process requires administration rights. After you have successfully installed the program "MIFAREdiscover" and all of its required components you can start "MIFAREdiscover" via the link

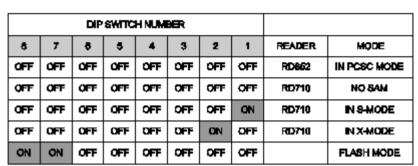
"Start -> All Programs -> NXP Semiconductors -> MIFAREdiscover -> MIFAREdiscover".

Read "ReleaseNotes.txt" file that you received with the MIFAREdiscover package.

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# 3. Demo mode and DIP switch configuration of the Pegoda

# 3.1 DIP switch configurations for various Reader modes



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Fig 8. Overview of important reader modi

You can find a photo with description on NXP web (see [35]) (table and figure 1)

#### 3.2 Demo mode

To get into demo mode, configure the DIP switch as follows

8	7	6	5	4	3	2	1
OFF	ON	OFF	OFF	OFF	OFF	OFF	OFF

and connect the USB cable. Only power is provided by the USB cable, the reader itself works autonomously without interaction of the PC.

The demo mode is used to showcase some basic functionality of the reader. In this mode, ISO14443-3A activation loop is performed and an acoustic signal is generated based on the detected card and **SAK-byte**, respectively.

The following table depicts the default sound coding for different MIFARE cards:

Table 1. Card type according to SAK and number of beep

Card Type	beep
MIFARE 1K (0x08)	1
MIFARE Classic 4K (0x18)	2
MIFARE Ultralight (0x00)	3
MIFARE DESFire	4
MIFARE Plus	5

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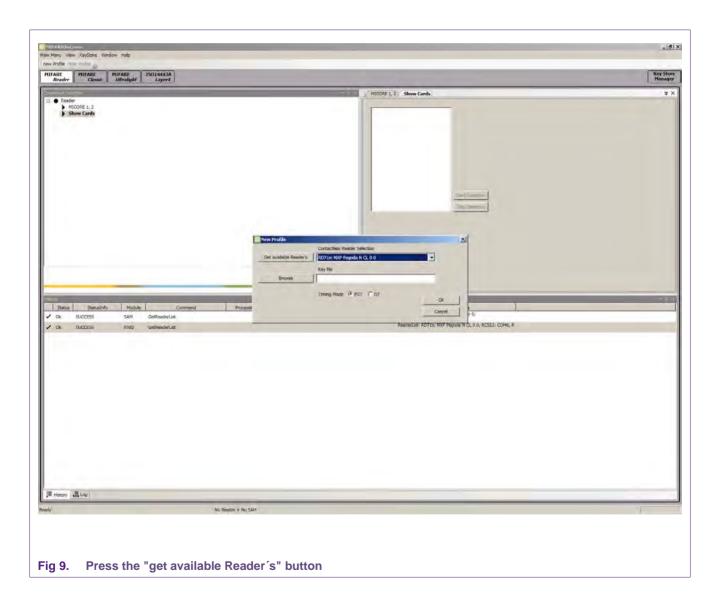
# 4. Public Version

All in this section explained steps are applicable to the full version as well.

# 4.1 Starting MIFAREdiscover

Connect the RD710 Reader with the PC by using the USB cable. Choose the desired DIP switch configuration on the mainboard of the reader (see chapter 3.1) as this tool does not support SAM. The DIP switches should all be set to "OFF".

Start MIFAREdiscover from the Start menu.

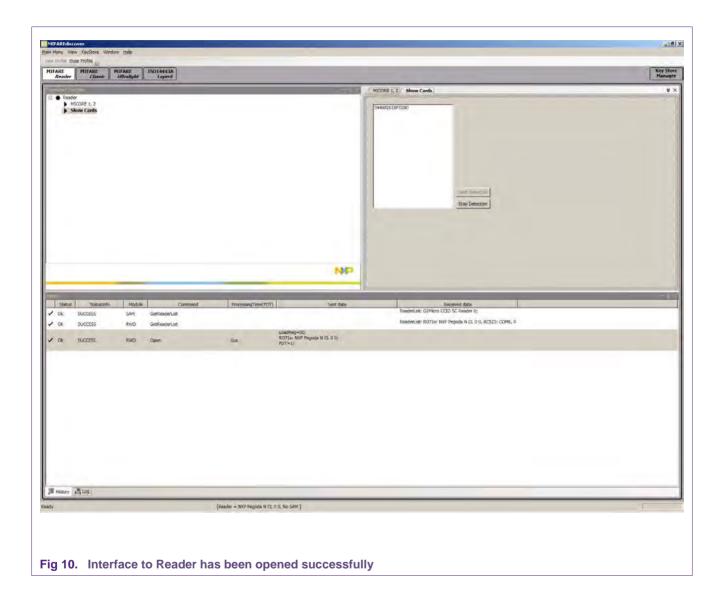


Press "Get available Reader's" for the drop down field "contact- and contactless reader selection". The available readers will be listed depending on the DIP switch configuration and the chosen reader.

Press "Ok" to open the mainframe of the MIFAREdiscover program for the specific reader configuration.

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The following main window will appear. The History frame shows you that the reader has been opened successfully. The configured reader mode can be depicted from the history list as well.



The "command selection window" in the left upper sub window shows you the different possibilities of the current selected reader device. For instance in MIFARE Reader tab is the possibility of running 'Show cards' command, in which you can see UID of present cards in RF Field.

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# 4.1.1 Mainframe general overview

The public MIFAREdiscover supports the functions for MIFARE Classic (see [1]), MIFARE Ultralight (see [13]), General ISO 14443-A (see [8]) protocol handling.

Therefore, the user interface is divided into functional blocks which are shown in different tabs.

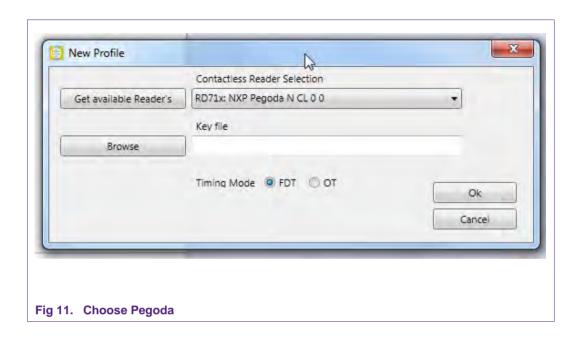
In every block the history field shows the operations (send data and receive data). This history field can be cleared, or it can be stored in a text file. For an illustration see figure 15.

Note: The sequence of commands as described in ISO/IEC 14443 or in the relevant datasheet must be kept to be able to activate and operate a card. The MIFAREdiscover does not cross check the logical command flow.

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# 5. Examples of some use cases for the public version

# 5.1 Accessing the MIFARE Classic



#### Ensure that the Pegoda reader is set to "Normal Mode".

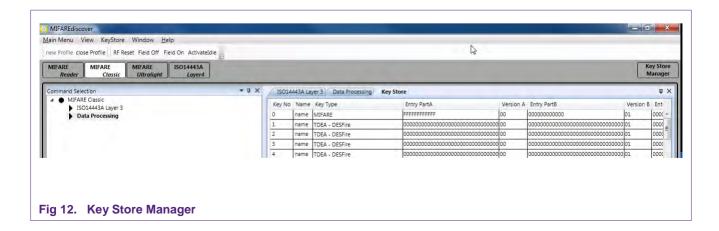
Choose the Pegoda as shown in figure 11 and press OK.

Now open the 'MIFARE Classic' Tab and select 'ISO 14443A Layer 3'. On the right side one can see a number of buttons for the activation of the card. The most convenient method is to push the 'Activate Idle' button.

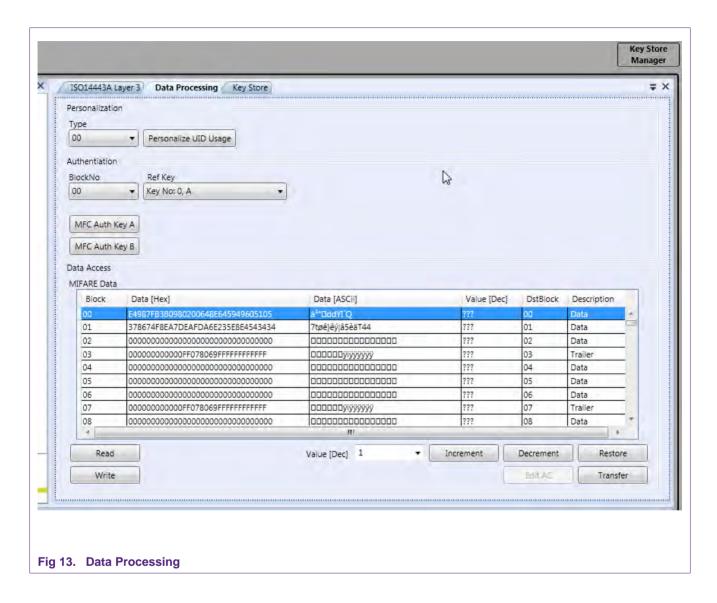
Now open the Key Store Manager and select the following settings for the first Key:

Key Type: MIFARE, Entry PartA: FFFFFFFFFF, Version: 0

Leave the Key Store Manager and select "Data Processing".



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Select BlockNo 00, Ref Key 0, A and click "MFC Auth Key A" Now you should be able to read and write in Sector 0 (Blocks 0-3).

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# 6. Full Version

# 6.1 Starting MIFAREdiscover

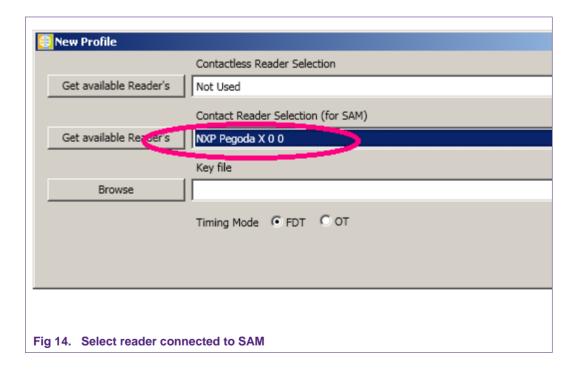
Connect RD710 Reader to the PC with USB cable. Choose the desired DIP switch configuration on the mainboard of the reader (see figure 8). This can be

- Reader in X-Mode for RD710 with MIFARE SAM inserted in the slot
- Reader in No SAM-Mode
- Reader in S-Mode

In the following descriptions we need to have the DIP switch of the Pegoda set to X-Mode.

Start MIFAREdiscover from the Start menu.

You will be asked to select your reader connected to MIFARE AV2 SAM as shown in Fig.14.



Select the reader. Any time the profile can be closed or opened by using close and open profile respectively.

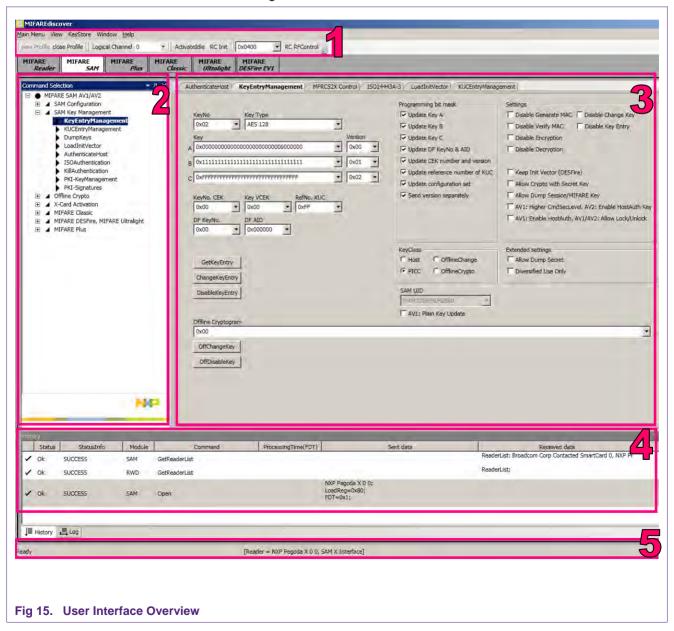
After selection of the reader the key file can be browsed if it is necessary. In the key file, the secret keys can be stored, which may be needed to authenticate MIFARE SAM AV2 with the host or to be changed later. Press OK button to validate the profile. If the MIFARE SAM AV2 is connected properly, the status field of the history window shows "SUCCESS".

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#### 6.2 User Interface Overview

There are 5 areas in the main window, as shown in Fig 15.

- 1. Menu Bar and Buttons: for reader connection, display settings and help
- 2. Command Selection Window: list of commands
- 3. Configuration Window: for detailed configurations of commands
- 4. History Window: Showing the command execution histories
- 5. Status Bar: Showing the current command execution status



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# 7. Examples of some use cases for the full version

In every use case (defined below) we need to have the DIP switch of the Pegoda reader set to **X-Mode**.

# Overview

# 7.1. Checking the connected MIFARE SAM AV2

Here you will get some hardware and software related information about the installed SAM.

#### 7.2. Switch the MIFARE SAM from AV1 to AV2 Mode

AV2 mode is recommended because of security reasons.

#### 7.3. Authenticate host

This step is needed when operating with the SAM.

#### 7.4. Operating the MIFARE DESFire EV1

# 7.4.1.Using MIFARE SAM AV2 for communication with MIFARE DESFire EV1

This example shows how to perform a basic authentication between SAM and MIFARE DESFire EV1.

#### 7.4.2. Create Application and format MIFARE DESFire

# 7.4.3. Authenticate Application

# 7.5. Operating the MIFARE Plus S

# 7.5.1. Switch MIFARE Plus from Security Level 0 in Security Level 1

Security level 0 is the initial delivery configuration of the PICC. We have to pre-personalize the card to get into security level 1.

#### 7.5.2. Switch MIFARE Plus from Security Level 1 in Security Level 3

Because security level 1 is the compatibility mode to the MIFARE Classic card, we want to use the enhanced security of security level 3.

# 7.5.3.Read/Write Actions of MIFARE Plus in Security Level 3

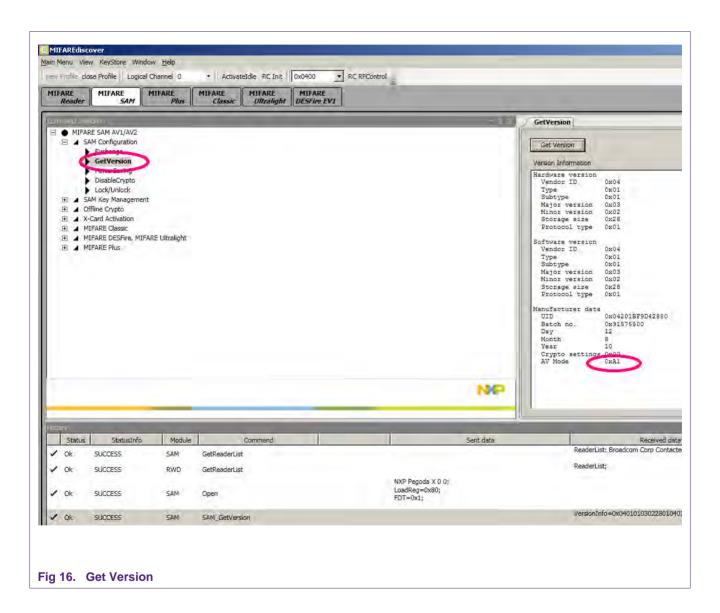
A short introduction of how to access blocks with read and write operation in security level 3.

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# 7.1 Checking the connected MIFARE SAM AV2

# Ensure that the Pegoda reader is set to "X-Mode".

Let's check the connected MIFARE SAM AV2. It can be done using the GetVersion command.



The last byte of the "GetVersion" response will be "0xA1" for MIFARE SAM AV1 and will be "0xA2" for MIFARE SAM AV2.

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# 7.2 Switch the MIFARE SAM from AV1 to AV2 Mode

The default MIFARE SAM is delivered from NXP semiconductor in MIFARE SAM AV1 mode. DIP switches should be set to "X-Mode" (see figure 8). For switching to AV2 mode follow the steps.

#### 7.2.1 Authenticate host

Select the "AuthenticateHost" command at the command window. Set the reference key as shown in the following figure. The reference key here used is "Key No: 0, A" for the default setting, where you need to authenticate host using SAM Master key entry and version "00" to change the key.

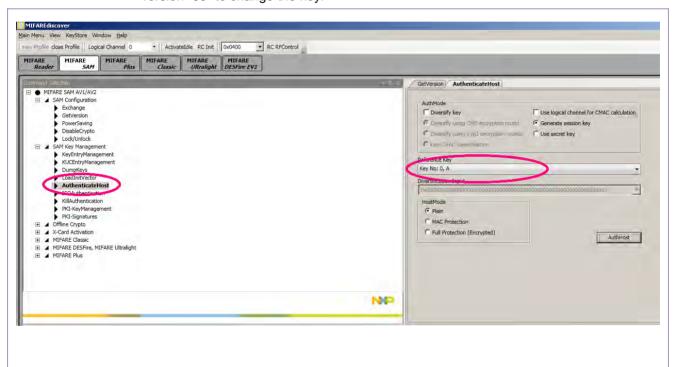


Fig 17. AuthenticateHost Key

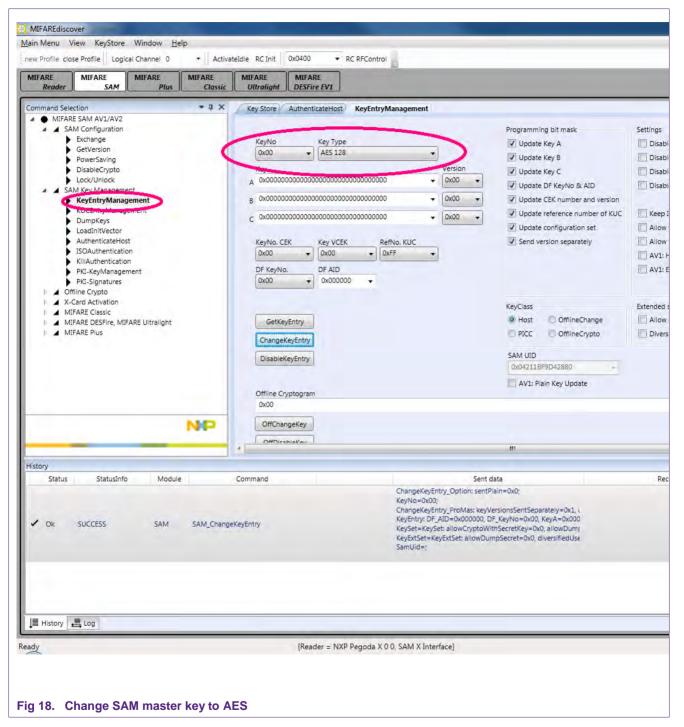
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# 7.2.2 Change SAM Master key to AES

SAM master key entry has to be changed to AES type to be able to switch the SAM mode.

Select the "KeyEntryManagement" Command from the command window and set the key "00" and key type "AES 128" (AES 192 is also ok). Set other field as shown in the following figure. We take the new key values and version all 00s, if you want, you can make your own keys as you like.

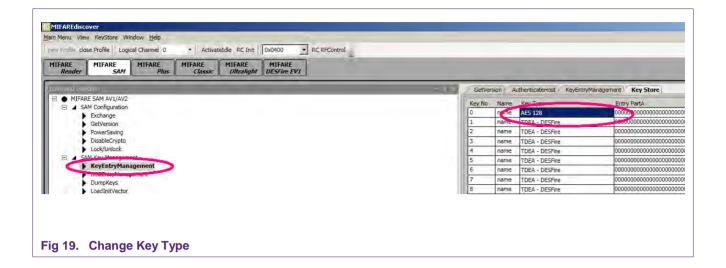
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If the SAM master key is already AES key type the steps 7.2.1 and 7.2.2 are not required.

Now change the key entry in the key file to have the same key as we have in the MIFARE SAM. Open the key file from menu. Change the key type and key values as we downloaded in previous command.

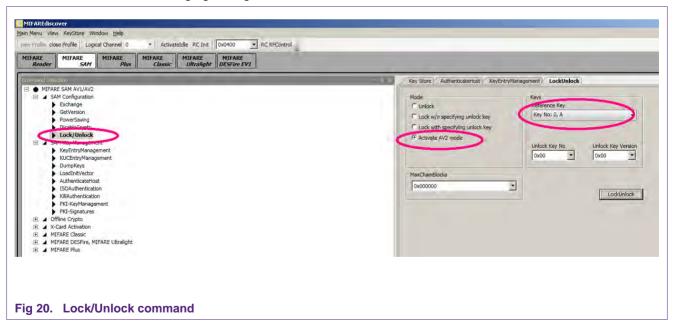
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#### 7.2.3 Lock/Unlock Command

Now the MIFARE SAM AV2 is ready to accept the Activation of AV2 mode command.

Select the "Lock/Unlock" Command from the command window. Set the mode to "Activate AV2 mode" and refer to the key "key no: 0, A" of the key file as shown in the following figure, figure 20.



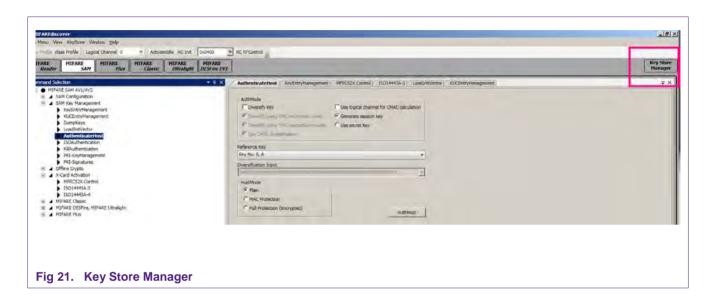
Now the MIFARE SAM is switched to AV2 mode.

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#### 7.3 Authenticate Host

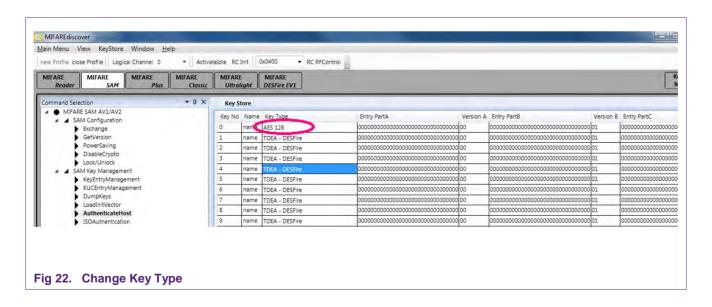
# Ensure that the Pegoda reader is set to "X-Mode".

Select the "AuthenticateHost" command at the command window and open the Key Store Manager.



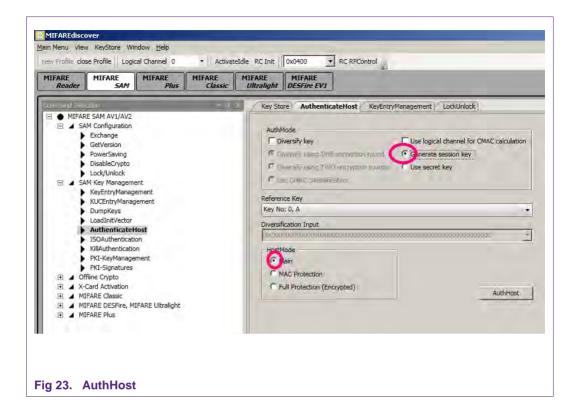
# Change the settings:

KeyNo: 0, Key Type: AES 128, Part A: 00000000000000000, VersionA: 00



Leave the Key Store manager and select "AuthenticateHost" again. Be sure to tick "Generate session key" at the AuthMode section and "Plain" at HostMode. Click "AuthHost".

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# 7.4 Operating the MIFARE DESFire EV1

The MIFARE DESFire EV1 answers every command with a status code. These codes can be found in MIFAREdiscover command selection at the Received data column. For example, if you enter an invalid command you will get "AppDataOut=0x1C".

A list with useful status and error codes is provided:

Table 2. Useful status and error codes

Hex Code	Status
0x00	OPERATION_OK
0x0C	NO_CHANGES
0x0E	OUT_OF_EEPROM_ERROR
0x1C	ILLEGAL_COMMAND_CODE
0x1E	INTEGRITY_ERROR
0x40	NO_SUCH_KEY
0x7E	LENGTH_ERROR
0x9D	PERMISSION_DENIED
0x9E	PARAMETER_ERROR
0xA0	APPLICATION_NOT_FOUND
0xA1	APPL_INTEGRITY_ERROR
0xAE	AUTHENTICATION_ERROR
0xAF	ADDITIONAL_FRAME
0xBE	BOUNDARY_ERROR

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Hex Code	Status
0xC1	PICC_INTEGRITY_ERROR
0xCD	PICC_DISABLED_ERROR
0xCE	COUNT_ERROR
0xDE	DUPLICATE_ERROR
0xEE	EEPROM_ERROR
0xF0	FILE_NOT_FOUND
0xF1	FILE_INTEGRITY_ERROR

# 7.4.1 Using MIFARE SAM AV2 for communication with MIFARE DESFire EV1

At first, the DESFire Key has to be downloaded to the MIFARE SAM if it is not already there. According to the steps done here, downloading a key to the MIFARE SAM requires host authentication as shown in § 7.3.

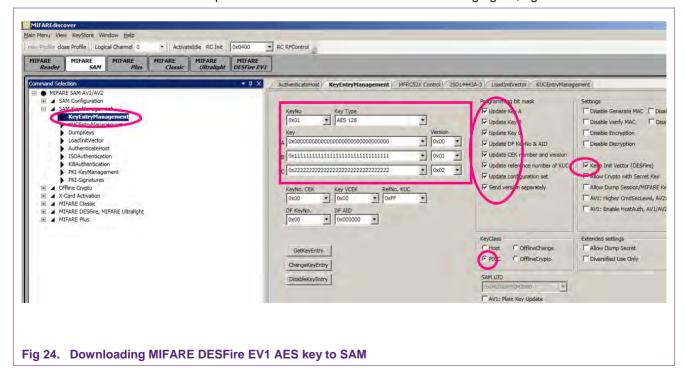
#### 7.4.1.1 Uploading MIFARE DESFire EV1 AES key to SAM

Let's change the key entry number 1 to PICC DESFire EV1 AES key. To make it simple

Key B = "111111111111111111111111111111111", version 0x01

Key C = "2222222222222222222222222222", version 0x02

The other options are checked as shown in the following figure, figure 24.



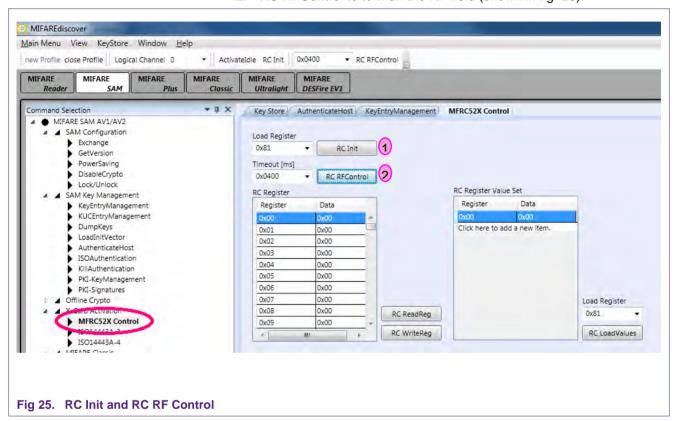
Do not forget to tick the "keep Init Vector" option for DESFire EV1 AES and standard TDEA keys.

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# 7.4.1.2 Accessing MIFARE DESFire EV1

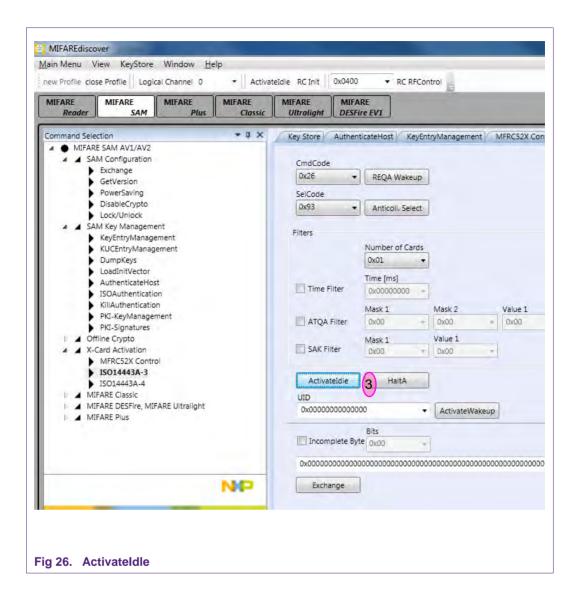
The steps are as follows:

- 1. RC Init to initialize the I<sup>2</sup>C communication (shown in fig. 25).
- 2. RC RFControl to turn on the RF field (shown in fig. 25).

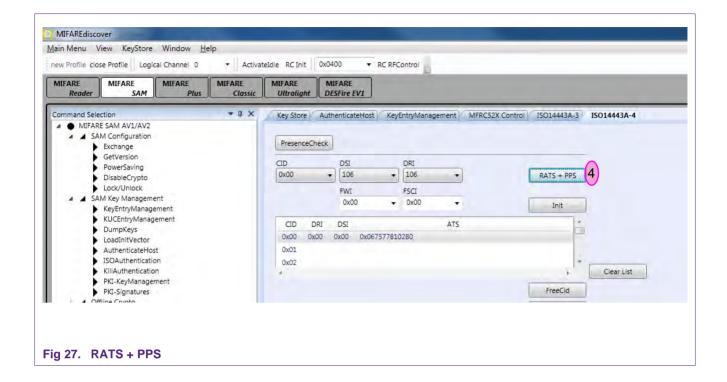


- 3. ActivateIdle to activate the MIFARE DESFire EV1 card to ISO/IEC14443 part 3 (shown in fig. 26).
- 4. RATS and PPS command to prepare the MIFARE DESFire EV1 card to ISO/IEC 14443-4 layer (shown in fig. 27).

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Sometimes, when opening RD710 with MIFARE SAM AV2 for the first time, ReqA and ActivateIdle commands fail. As a workaround, please execute the following steps.

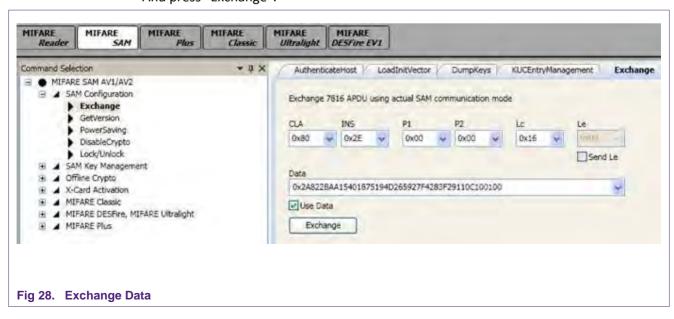
Select "SAM Configuration" and "Exchange" at the Command Selection window and insert the following data:

CLA = 0x80, INS = 0x2E, P1 = 0x00, P2 = 0x00, Lc = 0x16

Tick "Use Data"

Data: 2A822BAA15401875194D265927F4283F29110C100100

And press "Exchange".

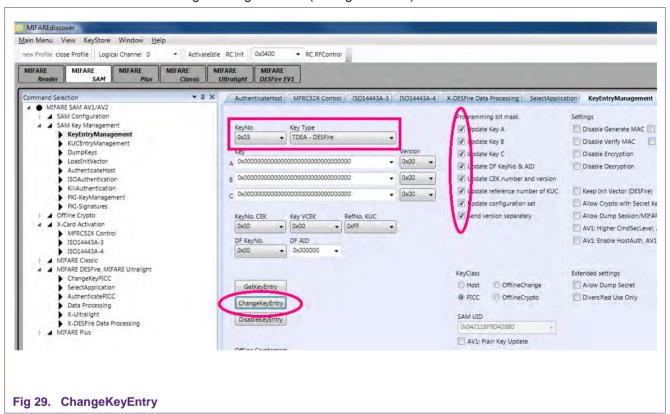


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# 7.4.2 Create Application and format MIFARE DESFire

- 1. See § 7.3 and do the AuthenticateHost command
- 2. Open the "KeyEntryManagement" and change the Master Key to KeyNo: 03 if it is still "default". The Key Type has to be TDEA DESFire.

All Keys and Versions are 0, KeyClass is PICC. Be sure to tick everything at Programming bit mask (see figure below).



- 3. Click "ChangeKeyEntry"
- 2. To get access to DESFire, do the steps described in § 7.4.1.2.
- Then select "X-DESFire Data Processing" at the Command Selection and insert the following data:

DESFire KeyNo:0

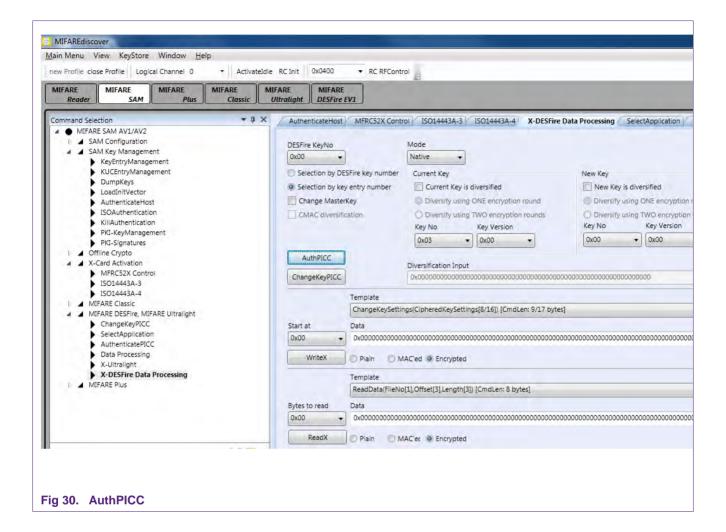
"Selection by key entry number"

Mode: Native

Current Key KeyNo: 0x03, KeyVersion 0x00

4. Click "AuthPICC".

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- 5. Go to "ISO14443A-4".
- 6. To see applications on the card, insert "0x6A" and click "Exchange".

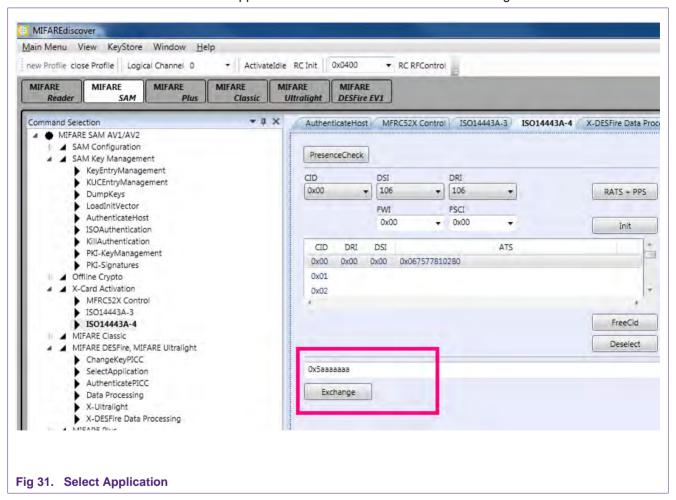
To create a new application, insert "0xCaaaaaaa0f8e" and click "Exchange" (The repeated "a" describe the AID (Application ID))

To format the card, insert "0xfc" and click "Exchange".

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# 7.4.3 Authenticate Application

- 1. Do the ApplicationHost command, as described in § 7.3.
- 2. Open the "KeyEntryManagement" and change the Master Key to "KeyNo: 01" if it is still "default". The Key Type has to be AES 128.
  - All Keys and Versions are 0, KeyClass is PICC. Be sure to tick everything at Programming bit mask and "Keep Init Vector (DESFire)".
- 3. Click "ChangeKeyEntry"
- 4. To get access to DESFire, do the steps described in § 7.4.2.
- 5. Select Application with "5aaaaaaa" and click "Exchange".



- 6. Select "X-DESFire Data Processing" at the Command Selection
- 7. Insert the following data:

DESFire KeyNo: 1

"Selection by key entry number"

Mode: Native

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Current Key: Key No.: 01, Key Version: 0

8. Click "AuthPICC".

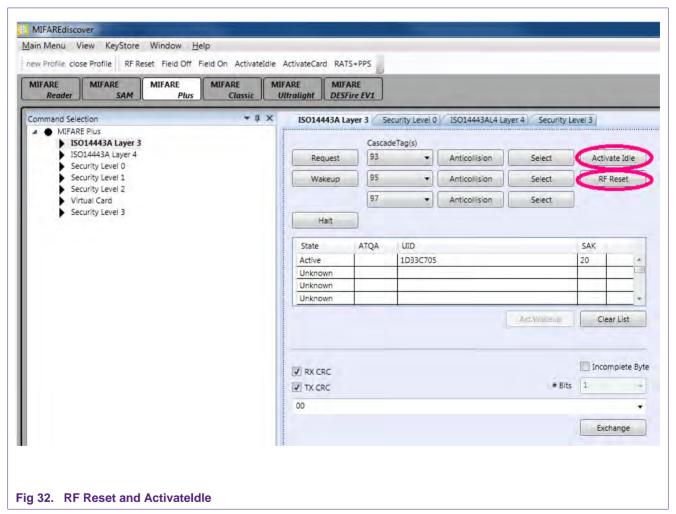
Now you are registered with your first Application.

# 7.5 Operating the MIFARE Plus S

To get to MIFARE Plus, you have to set the DIP switches to X-mode (see figure 8) and switch the MIFARE SAM into AV2 mode.

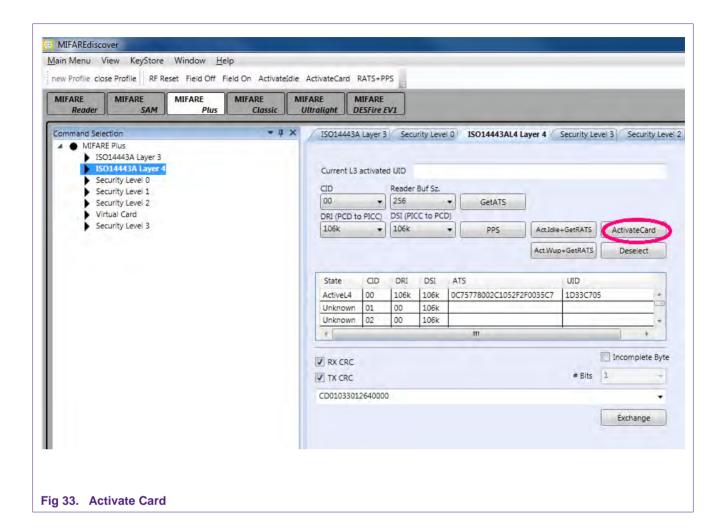
# 7.5.1 Switch MIFARE Plus from Security Level 0 in Security Level 1

Select "MIFARE Plus" (below the Menu bar) and then "ISO14443A Layer 3" at the command selection. Click "RF Reset" first and "Activate Idle" afterwards.



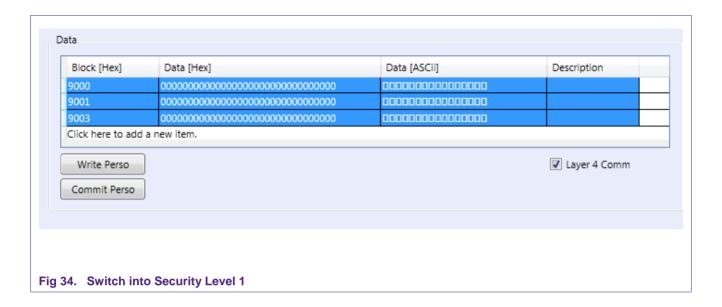
Go to "ISO14443A Layer 4" and click "ActivateCard".

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Select "Security Level 0" at the Command selection and insert the data as the next figure shows:



Press Ctrl on your keyboard to select all 3 entries. Then click "Write Perso" and afterwards "Commit Perso".

Then, press the following buttons (Menu bar) in this sequence: "RF Reset", "ActivateIdle", "ActivateCard".

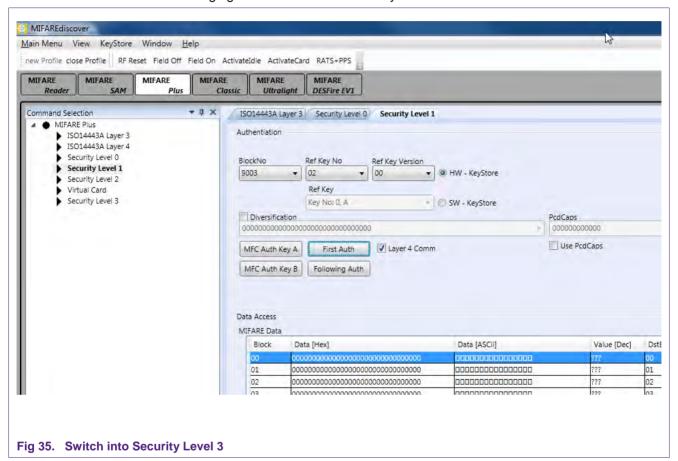
Now, your card is in Security Level 1.

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# 7.5.2 Switch MIFARE Plus from Security Level 1 in Security Level 3

After switching MIFARE Plus into Security Level 1, it is now possible to switch to Security Level 3.

Select "Security Level 1" at the Command selection and change the settings as shown in the following figure. Click "FirstAuth" after you are done.



Now your card is in Security Level 3.

You can check if the MIFARE Plus is in security level 3 by performing an "ActivateIdle" command. At the section "Received Data" in the command history you will see the SAK of the card afterwards. For example, if you have a 4KB MIFARE Plus card you will get 0x20 if it is in Security Level 3. See the Application note AN10833 for details. (http://www.nxp.com/documents/application\_note/AN10833.pdf)

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# 7.5.3 Read/Write Actions of MIFARE Plus in Security Level 3

Select "Security Level 3" at the Command selection.

Because the standard key for the blocks are the same as we inserted at the keystore at position 02, version 02, we can now use these to authenticate at different storage locations.

Change the settings:

Block/KeyNo: 4000

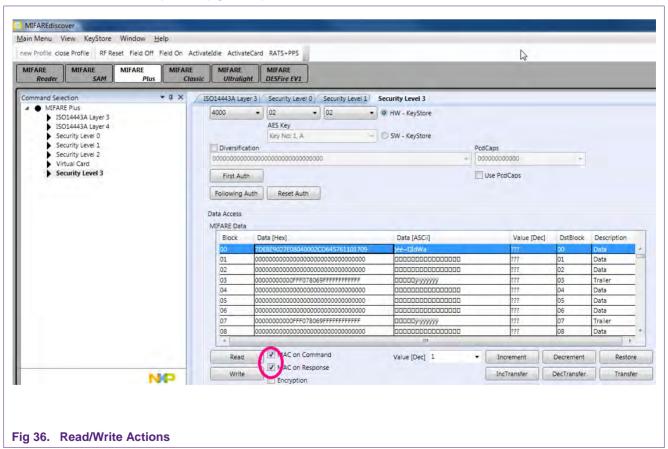
Ref Key No: 02

Ref Key Version: 02

"HW - KeyStore"

And click "FirstAuth"

If you click "Read" you can now read the individual blocks and if you click "Write" you can write beforehand edited blocks. Be sure to tick "MAC on Command" and "MAC on Response" (figure 35).



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# 7.6 Using MIFAREdiscover without MIFARE SAM AV2

Ensure that the Pegoda is set to "Normal Mode".

All described accesses to MIFARE cards are also possible without the MIFARE SAM AV2. If you use a MIFARE SAM AV2 all keys that are needed to get access to MIFARE cards get stored on the SAM.

If no MIFARE SAM AV2 is used the keys have to be inserted at the Key Store Manager of MIFAREdiscover. At positions where a key is needed you have to insert "SW – KeyStore" and the suitable storage location instead of "HWKeyStore".

# 8. Firmware download

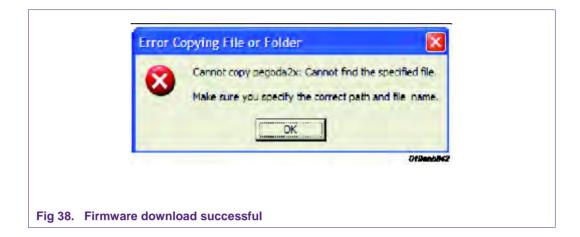
In order to download firmware to the reader, the DIP switches have to be configured to "flash mode". After a reset of the Pegoda reader it does response as mass storage device. The user can upgrade the Pegoda firmware by copying it (drag and drop) to the Pegoda mass storage device.

Important note: The file name of the binary file must be renamed to pegoda2x.bin, where x can be any character or letter or none; otherwise the Pegoda will refuse to copy the file and Windows will response with an I/O error (see 36).



After successfully copying the file, the Pegoda will start blinking and beeping; windows will response with the following message (see 37).

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Now, the firmware has been updated; reset the device and choose the desired reader mode with the DIP switches.

# 9. Error Codes

In MIFAREdiscover, user will observe following error and component codes in the History window under Status Info. The definitions of the error codes will help user to understand the cause of error.

# **Error codes & definitions**

Error Codes	Definition

SUCCESS Returned in case of no error

SUCCESS\_CHAINING Rx chaining is not complete, further action needed

SUCCESS\_INCOMPLETE\_BYTE An incomplete byte was received

IO\_TIMEOUT No reply received, e.g. PICC removal

INTEGRITY\_ERROR Wrong CRC or parity detected

COLLISION ERROR A collision occurred

BUFFER\_OVERFLOW Attempt to write beyond buffer size

FRAMING\_ERROR Invalid frame format

PROTOCOL\_ERROR Received response violates protocol

AUTH\_ERROR Authentication error

READ\_WRITE\_ERROR A Read or Write error occurred in RAM/ROM or Flash

TEMPERATURE\_ERROR The RC sensors signal overheating

RF\_ERROR Error on RF-Interface

INTERFACE\_ERROR An error occurred in RC communication

LENGTH ERROR A length error occurred

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INTERNAL ERROR An internal error occurred

INVALID DATA PARAMS Invalid data parameters supplied (layer id check

failed)

INVALID PARAMETER Invalid parameter supplied

PARAMETER OVERFLOW Reading/Writing a parameter would produce an

overflow

UNSUPPORTED PARAMETER Parameter not supported UNSUPPORTED COMMAND Command not supported USE\_CONDITION Condition of use not satisfied

**KFY** A key error occurred

# Component codes & Identifiers

Component Code Identifiers

**GENERIC** Generic Component Code BAL **BAL Component Code** HAL **HAL Component Code** 

PAL ISO14443P3A ISO14443-3A PAL-Component Code PAL ISO14443P3B ISO14443-3B PAL-Component Code PAL ISO14443P4A ISO14443-4A PAL-Component Code PAL ISO14443P4 ISO14443-4 PAL-Component Code PAL MIFARE MIFARE(R) PAL-Component Code PAL FELICA Open FeliCa PAL-Component Code PAL EPCUID ICode EPC/UID PAL-Component Code **PAL SLI15693** ICode SLI/ISO15693 PAL-Component Code

PAL I18000P3M3 ISO18000-3 Mode3 PAL-Component Code PAL I18092MPI ISO18092 passive initiator mode PAL-Component

Code

AL MFC MIFARE(R) Classic AL-Component Code AL MFUL MIFARE(R) Ultralight AL-Component Code AL MFP MIFARE(R) Plus AL-Component Code

AL VCA Virtual Card Architecture AL-Component Code

AL FELICA Open FeliCa AL-Component Code AL 115693 ISO15693 AL-Component Code AL SLI ICode SLI AL-Component Code

AL I18000P3M3 ISO18000-3 Mode3 AL-Component Code AL MFDF MIFARE DESFIRE EV1 AL Component Code

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AL\_P40CMDPRIV P40 command libraryAL-Component Code
AL\_P40CMDPUB P40 command libraryAL-Component Code

DL\_AMP Amplifier DL-Component Code

DL\_THSTRM Thermostream DL-Component Code
DL\_OSCI Oscilloscope DL-Component Code

DL\_RDFPGA Reader FPGA Box DL-Component Code

DL\_MSTAMPOSC Master Amplifier Oscilloscope DL-Component Code

DL\_STEPPERStepper DL-Component Code

CIDMANAGER
Cid Manager Component Code
CRYPTOSYM
CryptoSym Component Code
KEYSTORE
KeyStore Component Code
TOOLS
Tools Component Code

CRYPTORNG CryptoRng Component Code

LOG Log Component Code

# 10. References

- [1] **Datasheet** MF1S50 MIFARE Classic 1K Mainstream contactless smart card IC for fast and easy solution development, available on NXP web: http://www.nxp.com/documents/data\_sheet/MF1S50YYX.pdf
- [2] Datasheet MFR523; Contactless reader IC, BU-ID Doc. No. 1152\*\*1, available on NXP Web: http://www.nxp.com/documents/data\_sheet/MFRC523.pdf
- [3] **Datasheet** MFEV710, Pegoda EV710, available on NXP Web: http://www.nxp.com/documents/short\_data\_sheet/MFEV710\_SDS.pdf
- [4] Datasheet MIFARE DESFire; MF3ICDx21\_41\_81, MIFARE DESFire EV1 contactless multi-application IC, BU-ID Doc. No. 1340\*\*, available on NXP docu control
- [5] Datasheet MIFARE Plus; MF1PLUSx0y1, Mainstream contactless smart card IC for fast and easy solution development, BU-ID Doc. No. 163734, available at NXP docu control http://www.nxp.com/documents/short\_data\_sheet/MF1PLUSX0Y1\_SDS.pdf
- [6] Datasheet MIFARE Ultralight C; MF0ICU2, BU-ID Doc. No. 1714\*\*, available on NXP Web: http://www.nxp.com/documents/short\_data\_sheet/MF0ICU2\_SDS.pdf
- [7] Datasheet ICODE ILT, smart label IC; will be available on NXP Web
- [8] **ISO/IEC Standard** ISO/IEC14443 Identification cards Contactless integrated circuit cards Proximity cards
- [9] Datasheet MFRX852 Secure contactless reader solution, available on NXP web, Doc.-Id.: 1815\*\* http://www.nxp.com/documents/short\_data\_sheet/MFRX852\_SDS.pdf

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- [10] **Datasheet** MF1ICS50 MIFARE 1K, available on NXP web, Doc.-Id.: 0010\*\* http://www.nxp.com/documents/data\_sheet/001056.pdf
- [11] Datasheet MF1ICS70 MIFARE 4K, available on NXP web, Doc.-Id.: 0435\*\* http://www.nxp.com/documents/data\_sheet/043544.pdf
- [12] **Datasheet** MF0ICU2 MIFARE Ultralight C, available on NXP web, Doc.-Id.: 1714\*\* http://www.nxp.com/documents/short\_data\_sheet/MF0ICU2\_SDS.pdf
- [13] **Datasheet** MF0ICU1 MIFARE Ultralight, available on NXP web, Doc.-Id.: 0286\*\* http://www.nxp.com/documents/data\_sheet/MF0ICU1.pdf
- [14] **Datasheet** MF1PLUSx0y1 MIFARE Plus X, available on NXP web, Doc.-Id.: 1635\*\* http://www.nxp.com/documents/data\_sheet/MF1PLUSX0Y1\_SDS.pdf
- [15] Datasheet MF1SPLUSx0y1 MIFARE Plus S, available on NXP web, Doc.ld.: 1870\*\* http://www.nxp.com/documents/data\_sheet/MF1SPLUSX0Y1\_SDS.pdf
- [16] Datasheet MF3ICD21, MF3ICD41, MF3ICD81 MIFARE DESFire EV1, available on NXP web, Doc.-Id.: 1456\*\* http://www.nxp.com/documents/short\_data\_sheet/MF3ICDX21\_41\_81\_SDS.p df
- [17] Datasheet P5DF072EV2/T0PD4090 MIFARE SAM AV1, available on NXP web, Doc.-Id.: 1897\*\* http://www.nxp.com/documents/short\_data\_sheet/P5DF072EV2\_T0PD4090\_S DS.pdf

#### MIFARE SAM AV2:

- [18] **Datasheet** P5DF081 System Guidance, Delivery and Operation Manual http://www.nxp.com/documents/short\_data\_sheet/P5DF081\_SDS.pdf
- [19] Software MIFARE SAM AV2/ P5DF081 PES Reader Library
- [20] Application note MIFARE SAM AV2 Quick Start up Guide
- [21] Application note MIFARE SAM AV2 Interface and Architecture
- [22] Application note MIFARE SAM AV2 Key Management and Personalization
- [23] Application note AN10980 MIFARE SAM AV2 Host Communication
- [24] Application note AN10979 MIFARE SAM AV2 For MIFARE Plus
- [25] Application note AN1826 MIFARE SAM AV2 For MIFARE DESFire EV1
- [26] Application note AN1827 MIFARE SAM AV2 For MIFARE Ultralight C
- [27] Application note AN10978MIFARE SAM AV2 For MIFARE Classic
- [28] **Application note** AN10977 MIFARE SAM AV2 -X interface
- [29] Application note 1830 MIFARE SAM AV2 General Purpose Cryptography
- [30] Software MIFARE discover PC demo software for MIFARE SAM AV2
- [31] Objective Datasheet User Manual MIFAREdiscover

# Pegoda EV710:

- [32] **Application note** AN10990 Example Projects for NXP RD710/RD852 Readers, available on NXP web: http://www.nxp.com/documents/application\_note/AN10990.pdf
- [33] **Application note** AN10993 Pegoda Software Design Guide, available on NXP web: http://www.nxp.com/documents/application\_note/AN10993.pdf

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- [34] Application note AN10992 Quick Startup Guide for RD852 and RD710, available on NXP web: http://www.nxp.com/documents/application\_note/AN10992.pdf
- [35] **Application note** AN10991 RM710/RM852 Hardware Design Guide, available on NXP web: http://www.nxp.com/documents/application\_note/AN10991.pdf
- [36] **Application note** AN11002 Pegoda Toolchain Information, available on NXP web: http://www.nxp.com/documents/application\_note/AN11002.pdf
- [37] **Software** SW MIFARE discover, available on NXP web: http://www.nxp.com/documents/software/214410.zip
- [38] **Software** Pegoda RD710 Driver for 32 and 64 bit, available on NXP web: http://www.nxp.com/documents/software/RD710\_Driver\_214710.zip
- [39] **Software** MIFAREdiscover Public version, available on NXP web: http://www.nxp.com/pipMFEV710\_SDS.html
- [40] **Software** MIFAREdiscover Full version, available on doc store with doc ID.: 1717\*\*
- [41] Software .NET Framework, available online: http://www.microsoft.com/downloads/details.aspx?FamilyID=ab99342f-5d1a-413d-8319-81da479ab0d7&displaylang=en

<sup>1) ...</sup> BU-ID document version number

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RATP/Innovatron Technology

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